

CMOS or CCD image sensors for digital still applications?

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In this paper an in-depth comparison will be made between the good old Charge-Coupled Devices and the challenging CMOS image sensors as far as the digital still applications are considered. CMOS image sensors have the advantage of being cheaper, consuming less power and containing more electronics on-chip than the CCDs. On the other hand CCDs show superior image quality. What are the trade-offs between both technologies and are there other shortcomings when the devices are applied into a digital still application?